

FIG. 1

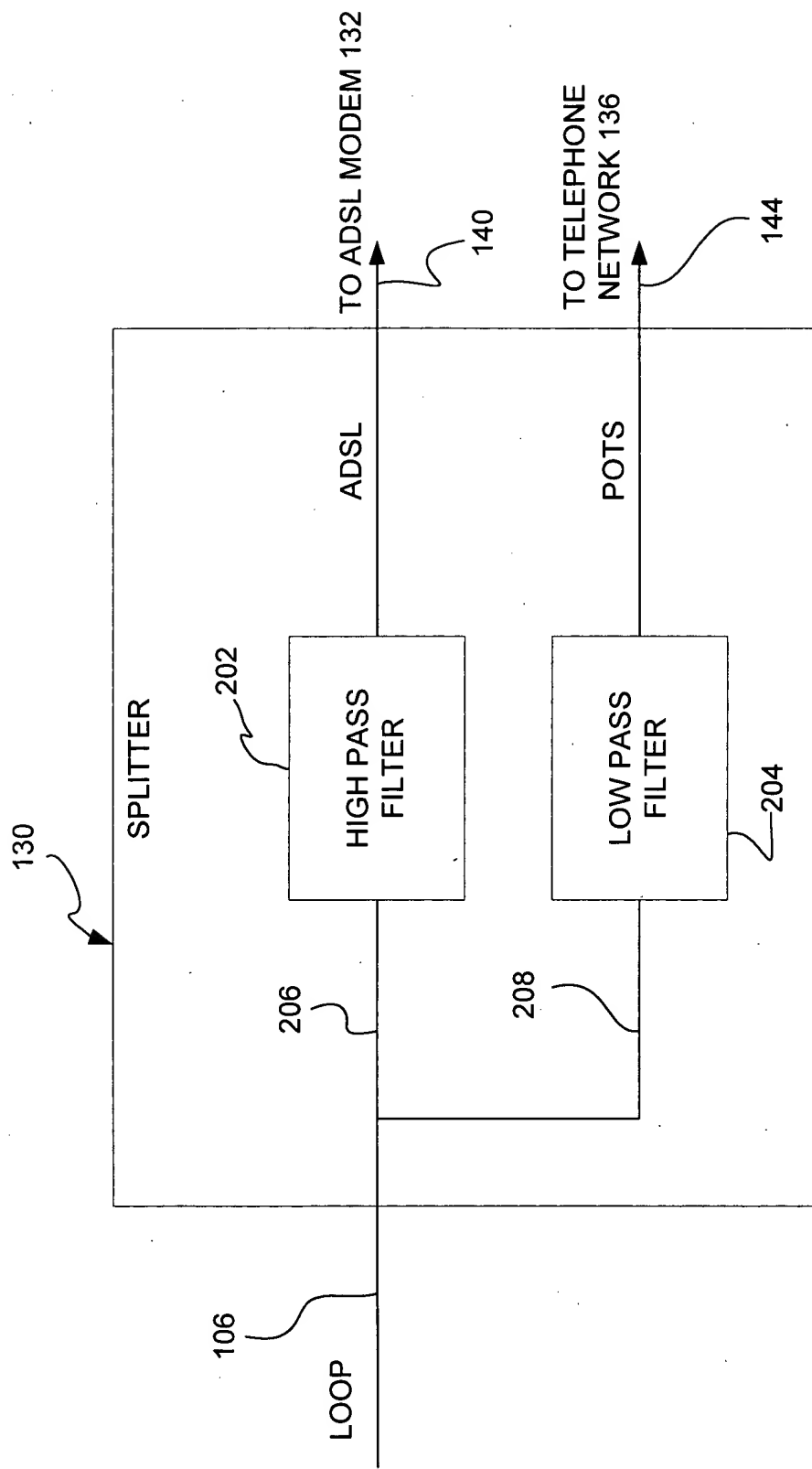


FIG. 2

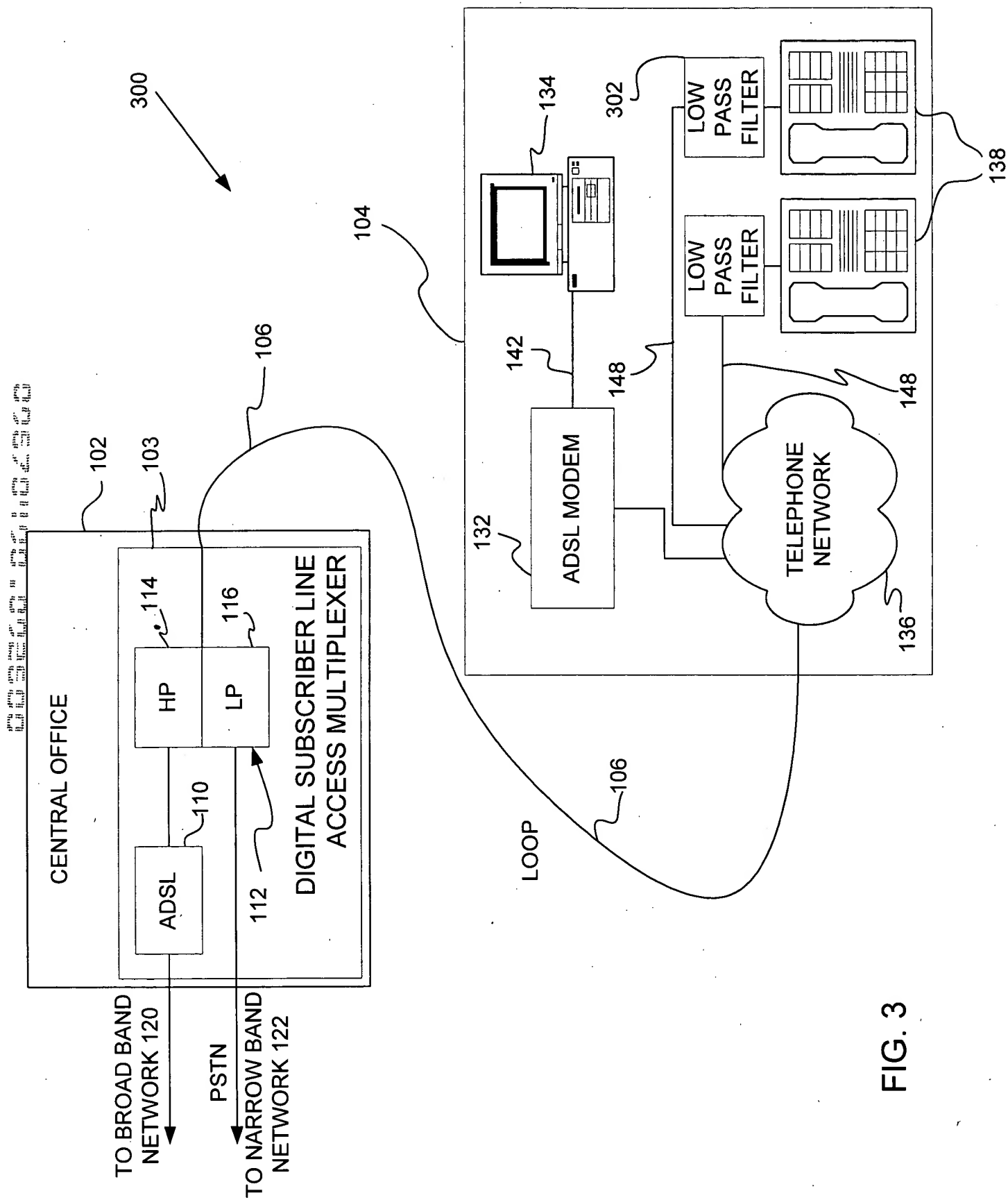


FIG. 3

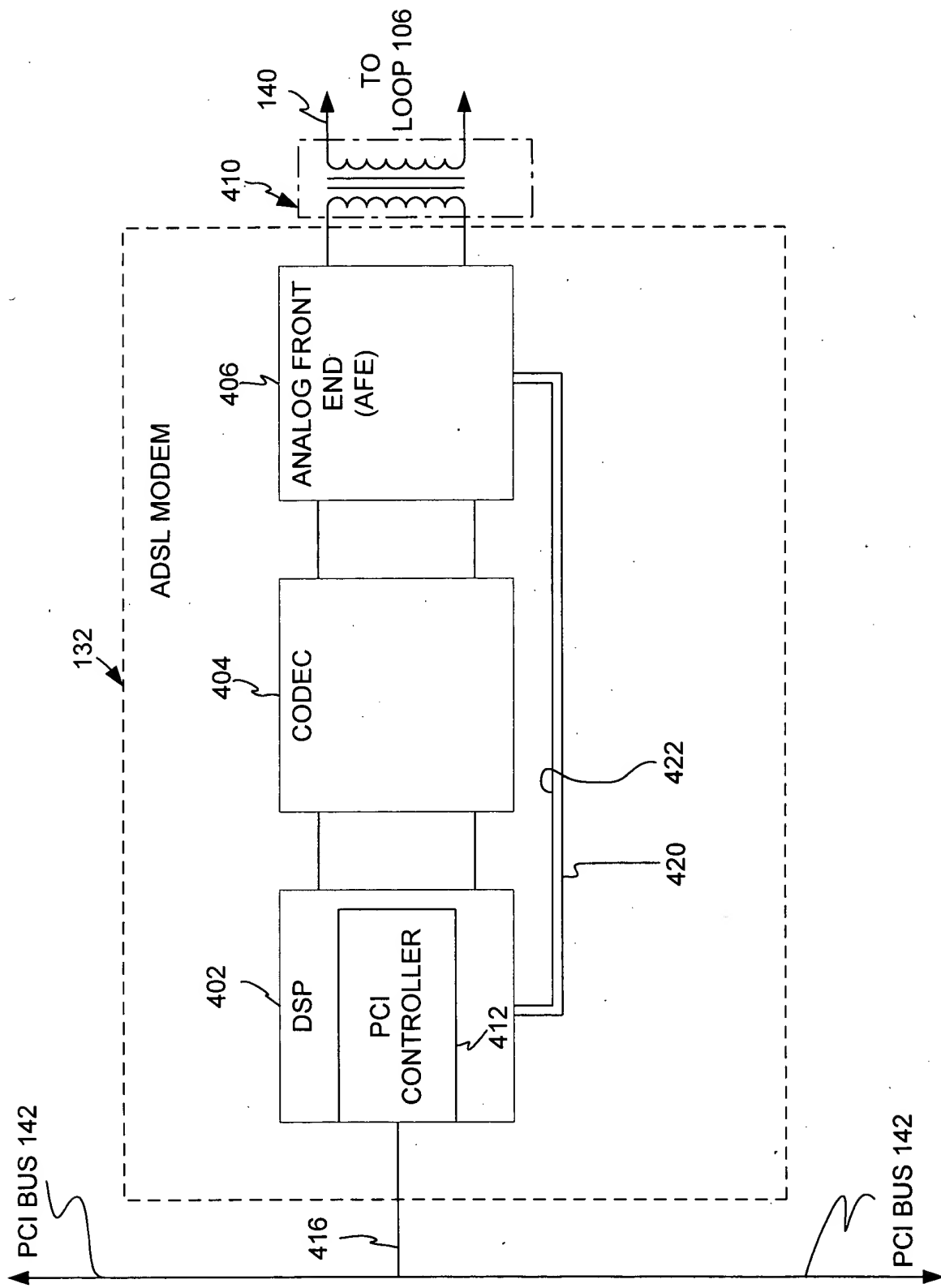


FIG. 4

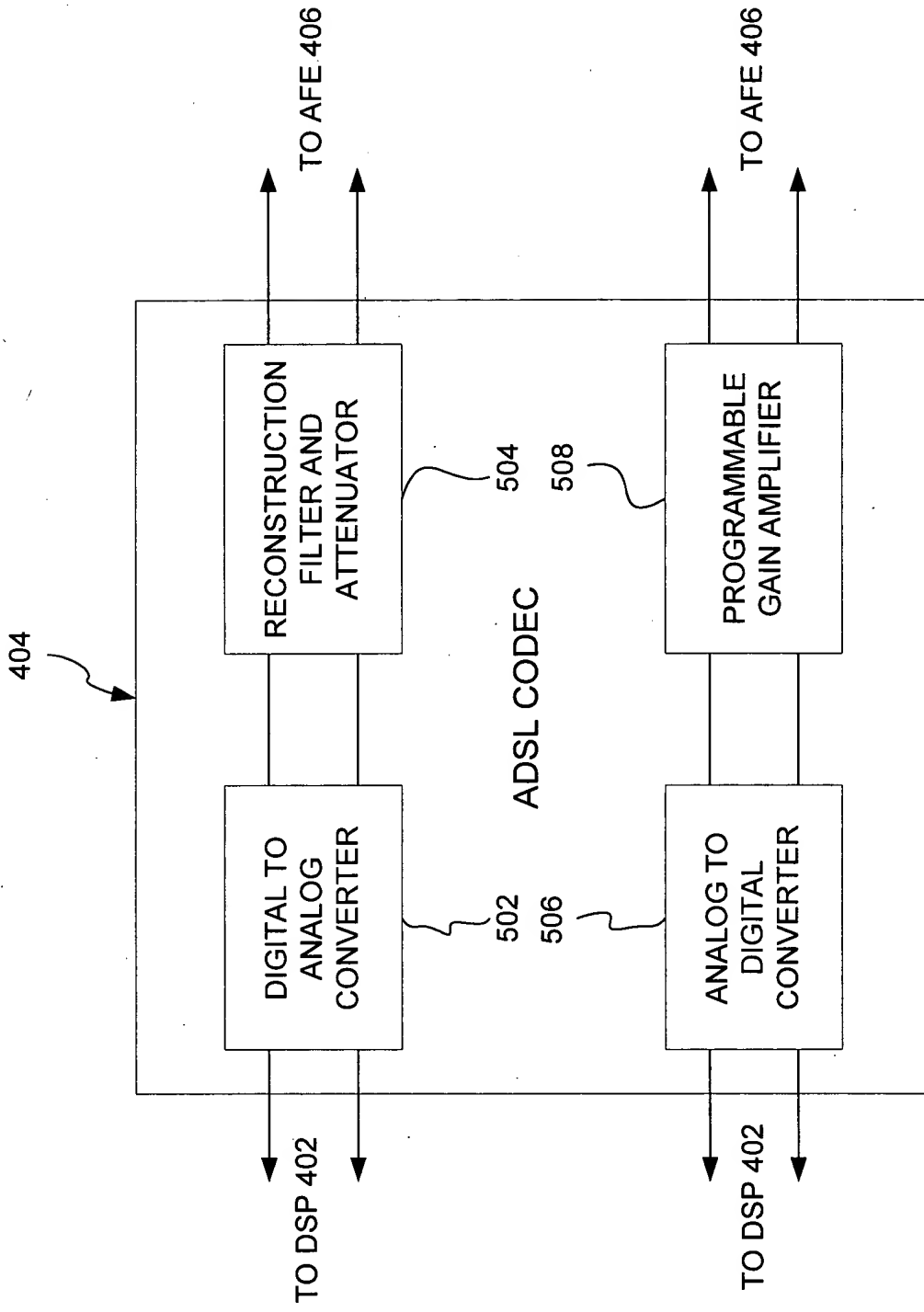


FIG. 5

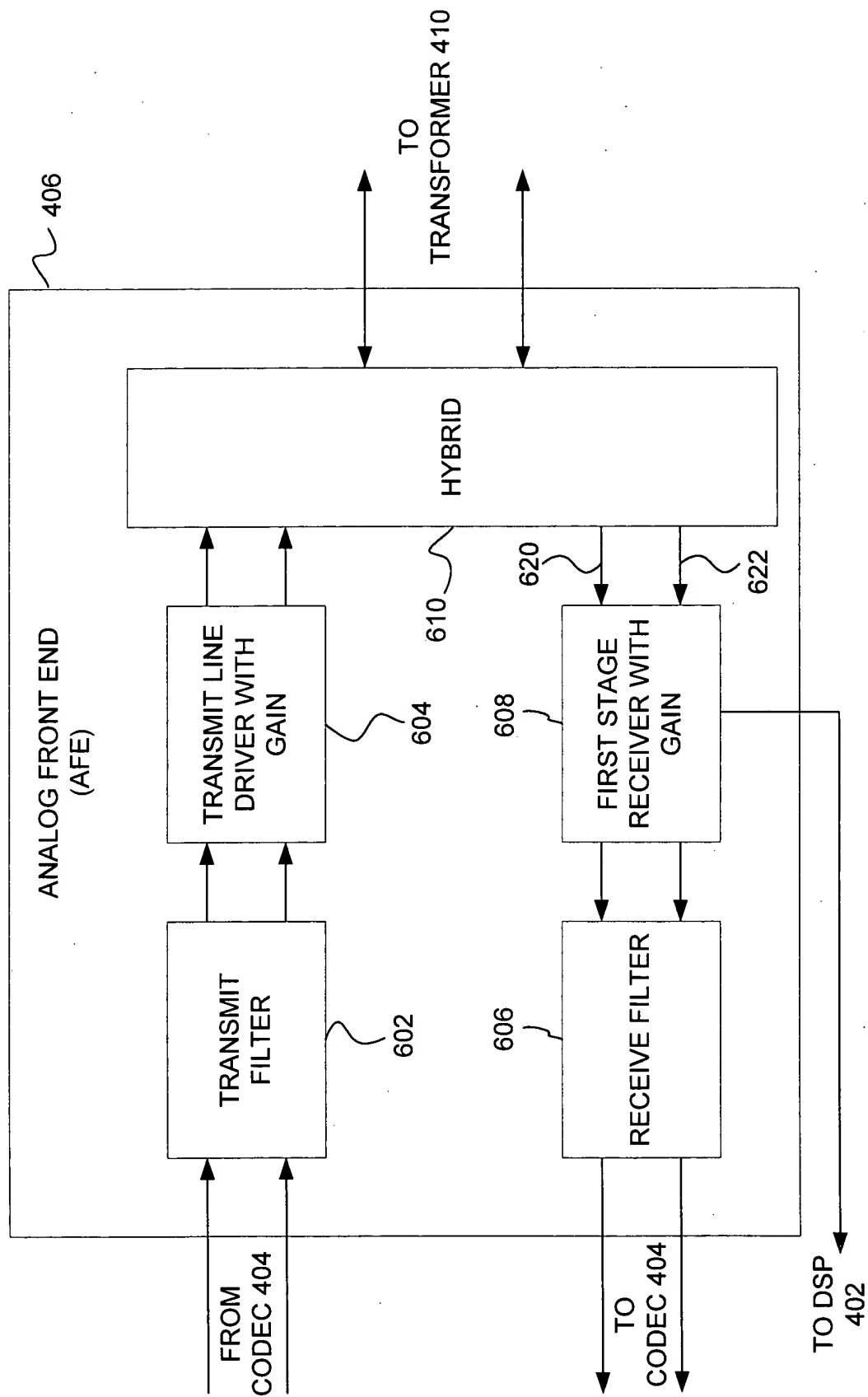


FIG. 6

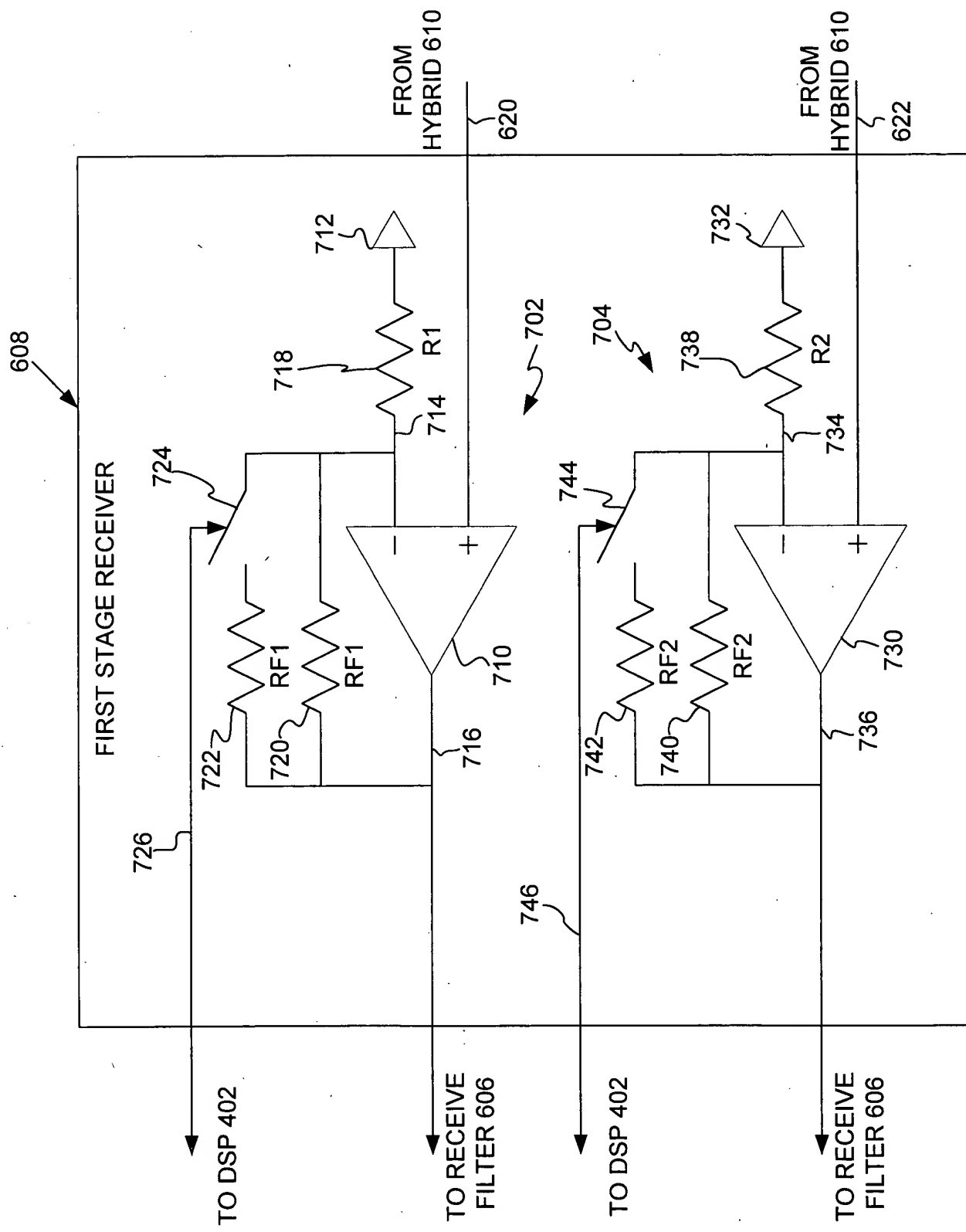


FIG. 7

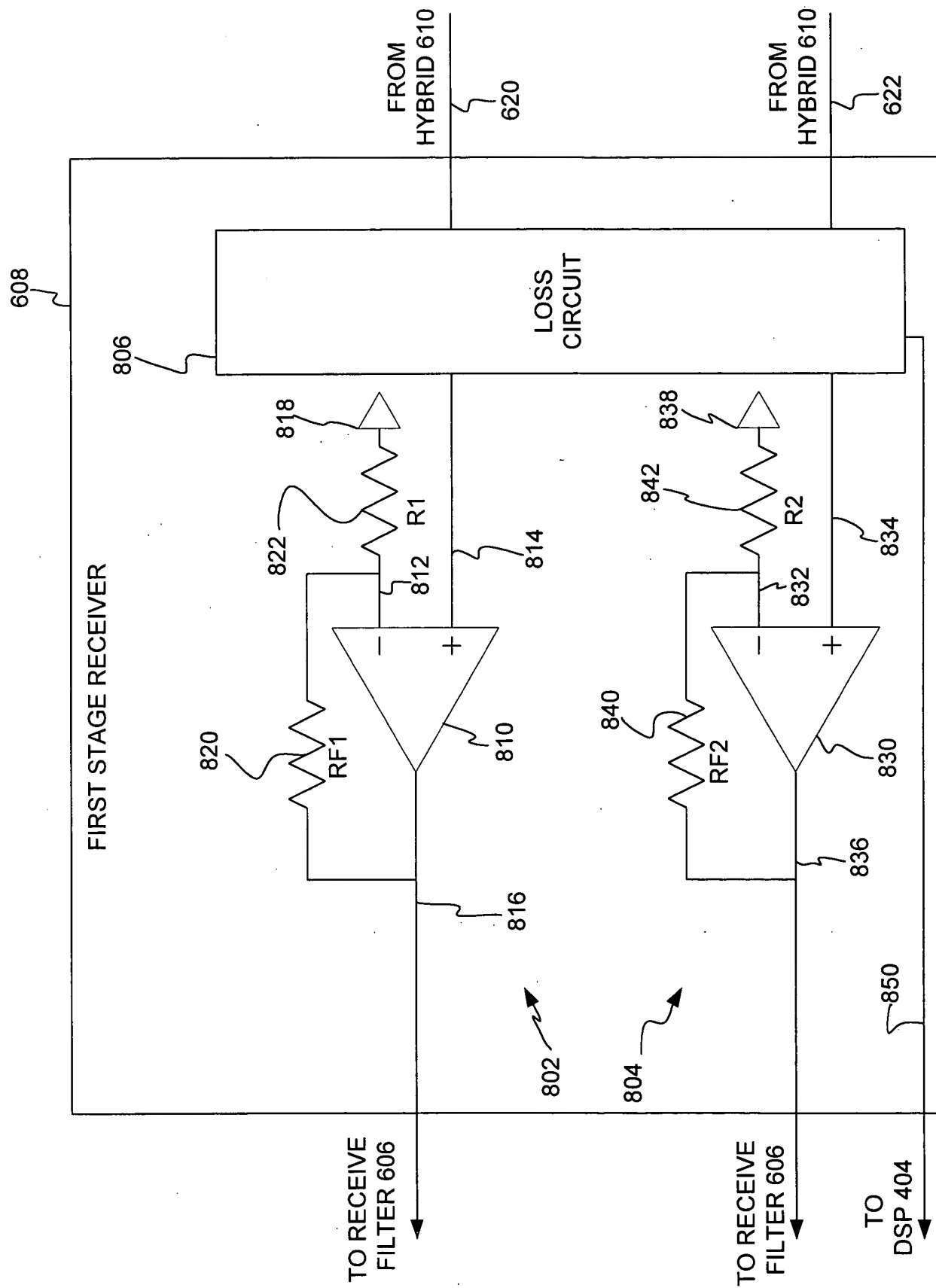


FIG. 8



FIG. 9 is a schematic diagram of a loss circuit 806, which is part of a system for processing signals from a hybrid 610. The circuit 806 includes a first resistor 620 connected to the hybrid 610 and a second resistor 622 connected to the hybrid 610. The circuit 806 also includes a third resistor 902 connected to the first resistor 620 and a fourth resistor 904 connected to the second resistor 622. The circuit 806 further includes a fifth resistor 906 connected to the third resistor 902 and a sixth resistor 910 connected to the fourth resistor 904. The circuit 806 is configured to provide a signal to an operational amplifier 810 and another signal to an operational amplifier 830. The circuit 806 also includes a seventh resistor 836 connected to the sixth resistor 910 and an eighth resistor 850 connected to the seventh resistor 836. The circuit 806 is configured to provide a signal to a digital signal processor (DSP) 402.

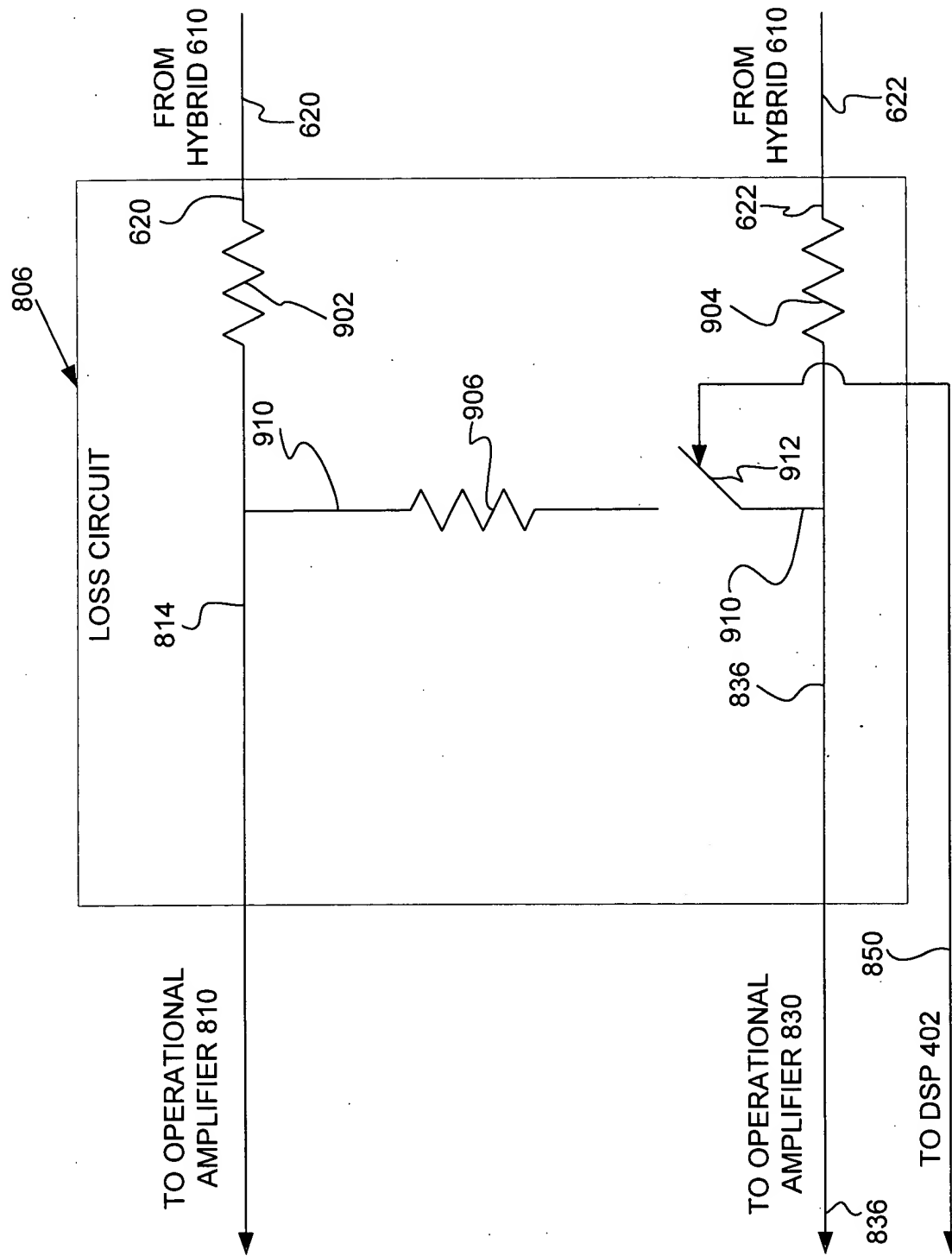


FIG. 9

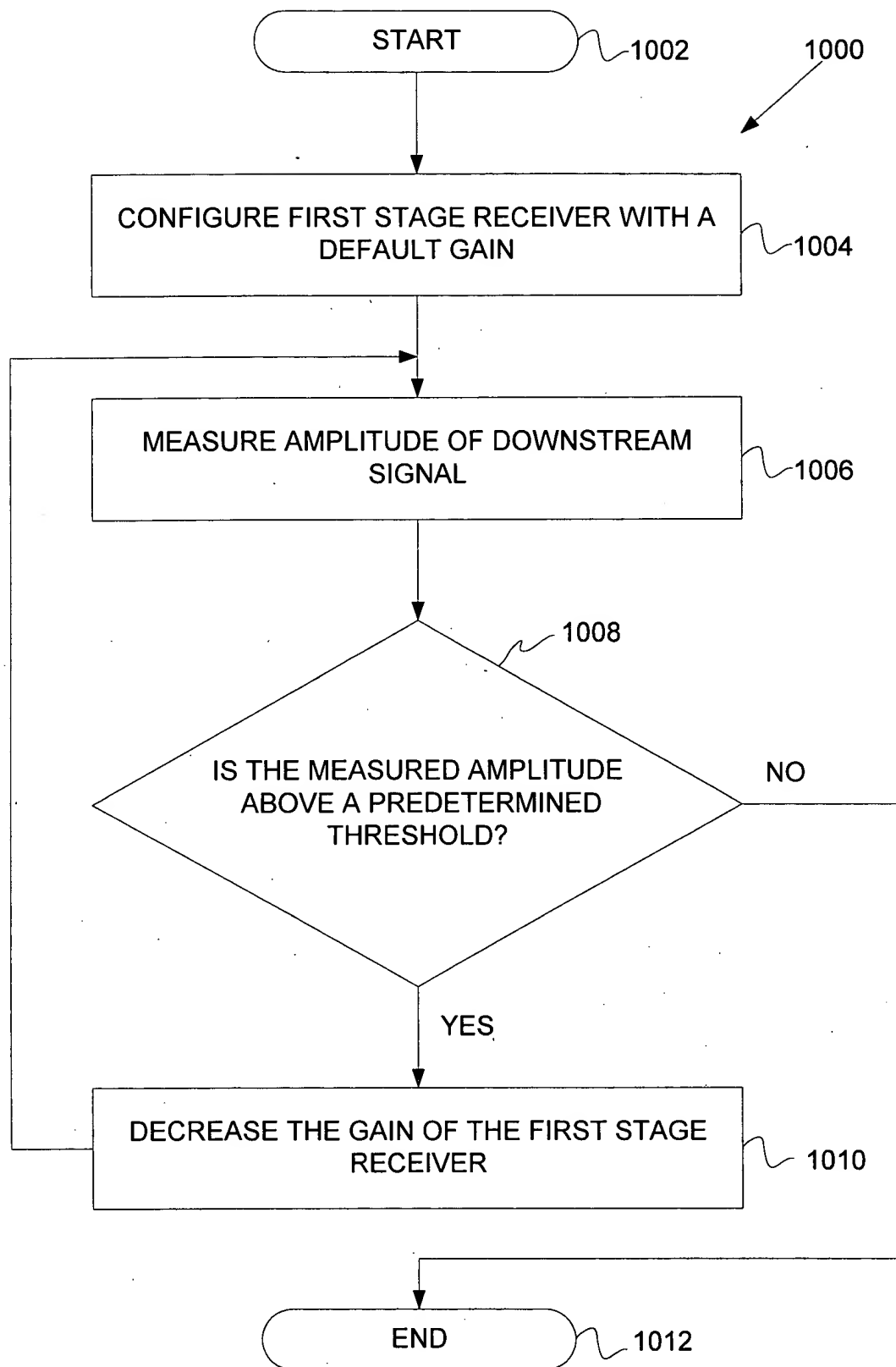


FIG. 10

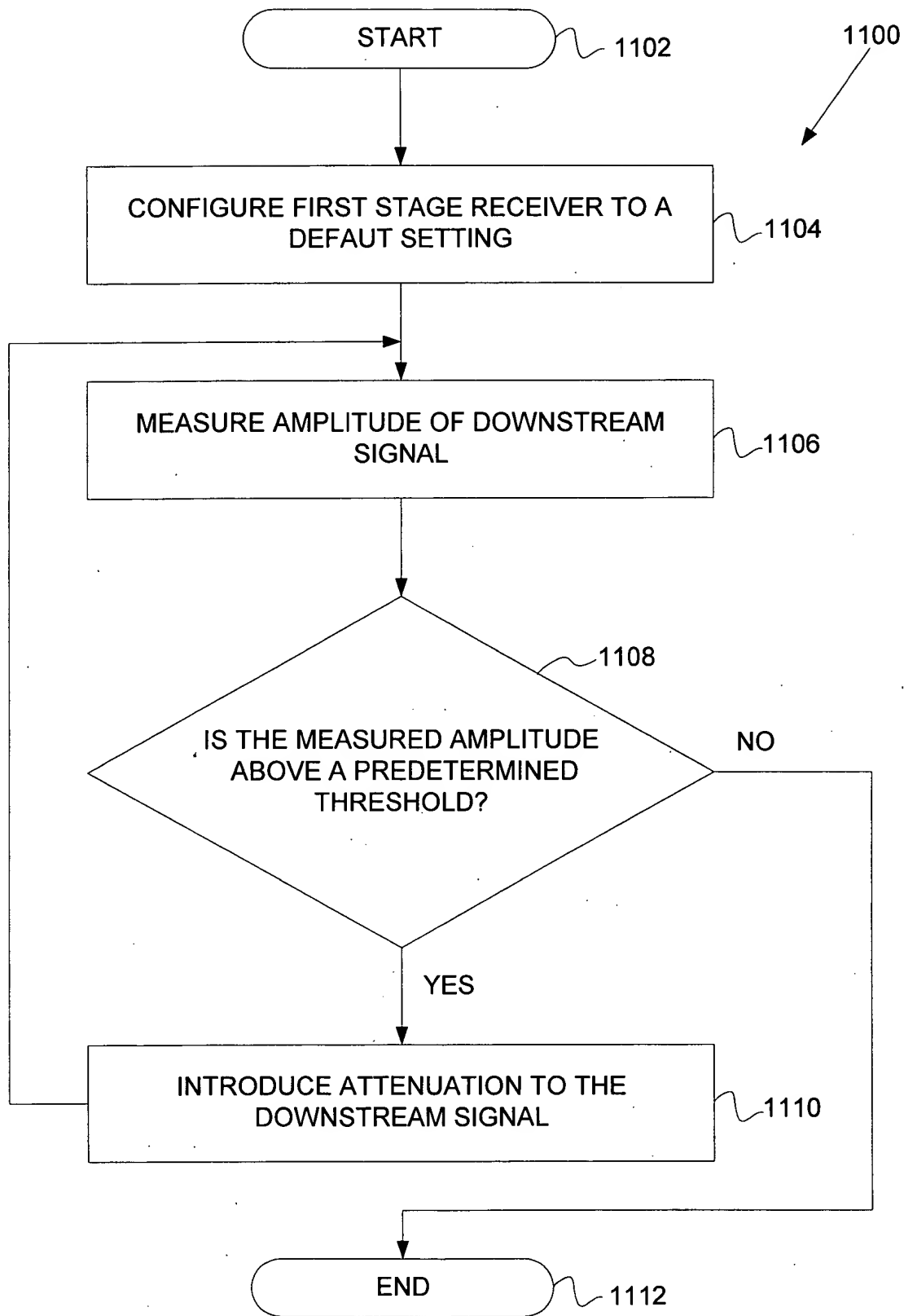


FIG. 11